_
-
Ω
Ø
Ν
0
Q
÷
J
۵
3
≷
>
₹
$\geq$
0
7
Ξ
_

· · · · · · · · · · · · · · · · · · ·					Code			
Electronics				Profile of study		0331131010330033 Year /Semester		
Field of study				(general academic, practical)				
	trol Engineering	and Robotics		(brak)		2/3		
Elective path/specialty				Subject offered in:  polish		Course (compulsory, elective) <b>obligatory</b>		
Cycle of	f study:		Foi	Form of study (full-time,part-time)				
First-cycle studies				full-time				
No. of h	ours					No. of credits		
Lectur	0.0000			Project/seminars:	-	5		
Status o	· · · · · · · · · · · · · · · · · · ·	program (Basic, major, other)		(university-wide, from another fi	,	<b>L</b> \		
Education	on areas and fields of sci	(brak) ence and art			(bra	ECTS distribution (number		
						and %)		
techr	technical sciences				;	5 100%		
Resp	onsible for subj	ect / lecturer:						
	nż. Jan Deskur							
	ail: Jan.Deskur@put.p +48 61 665 2735	oznan.pl						
Wyd	dział Elektryczny							
	Piotrowo 3A 60-965 Po							
Prere	equisites in term	s of knowledge, skills and	d s	ocial competencies:				
1	Knowledge	K_W02:						
2	Skills	K_U01: K_U04:						
3	Social competencies	K_K_02:						
Assu	-	ectives of the course:						
Knowle	edge concerning princ	iples of operation of the electronic	circ	cuits;				
the abi	•	as designing the electronic circuit						
	-	mes and reference to the	ed	ucational results for	a fi	eld of study		
Know	/ledge:							
	/_12 - [K_W12]							
Skills								
1. K_U15 - [K_U15]								
2. K_U20 - [K_U20] 3. K_U23 - [K_U23]								
Social competencies:								
1. K_K04 - [K_K04]								
Assessment methods of study outcomes								
	·	nination in semester 4	O 114	on laboratori				
- Laboratory: attendance in exercises, evaluation of written reports on laboratory exercises.  Course description								
1		Course a	C2(	cription				

STUDY MODULE DESCRIPTION FORM

# **Faculty of Electrical Engineering**

- Lectures: Passive electronic components. Semiconductor materials; p-n junction. Diodes, its models and applications, Bipolar transistors, field effect transistors. Integrated circuits of small and medium scale of integration. Operational amplifiers.
- . Applications of operational amplifiers to analogue signal processing. Analogue controllers and filters. Electronic switches, S&H, DA and AD converters; switched capacitors devices. Selected problems of industrial electronics.

Laboratory: diodes, transistors, operational amplifiers, filters; circuit-oriented simulation programs.

### Basic bibliography:

- 1. Lecture materials provided by the teacher in electronic form
- 2. Elektronika. Układy elektroniczne, Waldemar Nawrocki, WPP, Poznań 2010
- 3. Wprowadzenie do elektroniki i energoelektroniki, Marian P. Kaźmierkowski, Jerzy T. Matysik, Oficyna Wyd. Pol. Warszawskiej, Warszawa, 2005

### Additional bibliography:

- 1. Układy półprzewodnikowe, Ulrich Tietze, Christoph Schenk, WNT, Warszawa, 1996,2009
- 2. Elementy i układy elektroniczne w pytaniach i odpowiedziach, Mirosław Rusek, Jerzy Pasierbiński, WNT, Warszawa, 2006

## Result of average student's workload

Activity	Time (working hours)
1. Lecturew	30
2. Laboratory	30
3. Preparation to laboratory excersises, elaboration of reports	30
4. Home excersises	15
5. Preparation to tests/examination	15
6. Examination	5

#### Student's workload

Source of workload	hours	ECTS
Total workload	125	5
Contact hours	65	3
Practical activities	60	2